

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Niall D. McDonnell	Art Unit	: 2616
Serial No.	: 10/716,376	Examiner	: Brenda H. Pham
Filed	: November 17, 2003		
Assignee	: Intel Corporation		
Title	: TIMESLOT ASSIGNMENT		

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Notice of Allowance Date: July 1, 2008

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed July 1, 2008, enclosed is a completed issue fee transmittal form PTOL-85b.

COMMENTS ON EXAMINER'S REASONS FOR ALLOWANCE

Applicant recognizes that in accordance with M.P.E.P. § 1302.14, the Examiner's reasons for allowance need not set forth all of the details as to why the claims are allowed.

Applicant does not concede that the Examiner's stated reasons for allowance are the only reasons for which the claims are allowable. The claims may be allowable for other reasons as well. In particular, Applicant does not concede that all of the limitations identified by the Examiner are necessary to distinguish the prior art of record or to satisfy the requirements of 35 U.S.C. § 112. In addition, the Examiner does not assert, and Applicant would not concede, that the Examiner's reasons have any bearing on the patentability of claims in any other applications directed to the disclosed subject matter.

In addition, each dependent claim stands on its own and is allowable on its own merits. In particular, each dependent claim may be allowable on the basis of a combination of some of

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the features recited in the dependent claim and its base claim(s), which combination of features may not include all of the limitations identified in the Examiner's reasons for allowance.

Please apply the required fees in the amount of \$1,740 to our Deposit Account
No. 06-1050.

Respectfully submitted,

Date: October 1, 2008

/Sushil Shrinivasan L0368/
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